

IN THE CLAIMS

A listing of all claims and their current status in accordance with 37 C.F.R. § 1.121(c) is provided below.

1. (Currently Amended) A holder having a plurality of semiconductor die stacks thereon, the holder configured being adapted to temporarily hold the plurality of semiconductor die stacks, wherein each of the plurality of semiconductor die stacks include at least two semiconductor die coupled together by an adhesive and wherein the plurality of semiconductor die stacks do not include a lead frame or a substrate.
2. (Previously Presented) The holder, as set forth in claim 1, comprising a tape reel having the plurality of semiconductor die stacks thereon.
3. (Withdrawn) The holder, as set forth in claim 1, comprising a gel pack having the plurality of semiconductor die stacks thereon.
4. (Withdrawn) The holder, as set forth in claim 1, comprising a tray having the plurality of semiconductor die stacks thereon.
5. (Currently Amended) The holder, as set forth in claim 1, wherein each of the plurality of semiconductor die stacks comprises at least ~~two~~ three semiconductor die, each of the semiconductor die being coupled together by ~~an~~ the adhesive.

6. (Currently Amended) The holder, as set forth in claim 1 5, wherein one of the at least two semiconductor die is thicker than a second of the at least two semiconductor die.

7. (Currently Amended) The holder, as set forth in claim 1 5, wherein the topside surface area of one of the at least two semiconductor die is less than the topside surface area of a second of the at least two semiconductor die.

8. (Currently Amended) The holder, as set forth in claim 1 5, wherein the stack of at least two semiconductor die is configured such that the stack comprises a shingle stack.

9. (Currently Amended) The holder, as set forth in claim 1 5, wherein at least one of the at least two semiconductor die comprises a memory die.

10. (Currently Amended) A tape reel having a plurality of semiconductor die stacks thereon, the tape reel ~~being adapted~~ configured to temporarily hold the plurality of semiconductor die stacks, wherein each of the plurality of semiconductor die stacks include at least two semiconductor die coupled together by an adhesive and wherein the plurality of semiconductor die stacks do not include a lead frame or a substrate.

11. (Currently Amended) The tape reel, as set forth in claim 10, wherein each of the plurality of semiconductor die stacks comprises at least ~~two~~ three semiconductor die, each of the semiconductor die being coupled together by ~~an~~ the adhesive.

12. (Currently Amended) The tape reel, as set forth in claim 10 ~~11~~, wherein one of the at least two semiconductor die is thicker than a second of the at least two semiconductor die.

13. (Currently Amended) The tape reel, as set forth in claim 10 ~~11~~, wherein the topside surface area of one of the at least two semiconductor die is less than the topside surface area of a second of the at least two semiconductor die.

14. (Currently Amended) The tape reel, as set forth in claim 10 ~~11~~, wherein the stack of at least two semiconductor die is configured such that the stack comprises a shingle stack.

15. (Currently Amended) The tape reel, as set forth in claim 10 ~~11~~, wherein at least one of the at least two semiconductor die comprises a memory die.

16. (Withdrawn) A gel pack having a plurality of semiconductor die stacks thereon, the gel pack being adapted to temporarily hold the plurality of semiconductor die stacks.

17. (Withdrawn) The gel pack, as set forth in claim 16, wherein each of the die stacks comprises at least two semiconductor die, each of the semiconductor die being coupled together by an adhesive.

18. (Withdrawn) The gel pack, as set forth in claim 16, wherein one of the at least two semiconductor die is thicker than a second of the at least two semiconductor die.

19. (Withdrawn) The gel pack, as set forth in claim 16, wherein the topside surface area of one of the at least two semiconductor die is less than the topside surface area of a second of the at least two semiconductor die.

20. (Withdrawn) The gel pack, as set forth in claim 16, wherein the stack of at least two semiconductor die is configured such that the stack comprises a shingle stack.

21. (Withdrawn) The gel pack, as set forth in claim 16, wherein at least one of the at least two semiconductor die comprises a memory die.